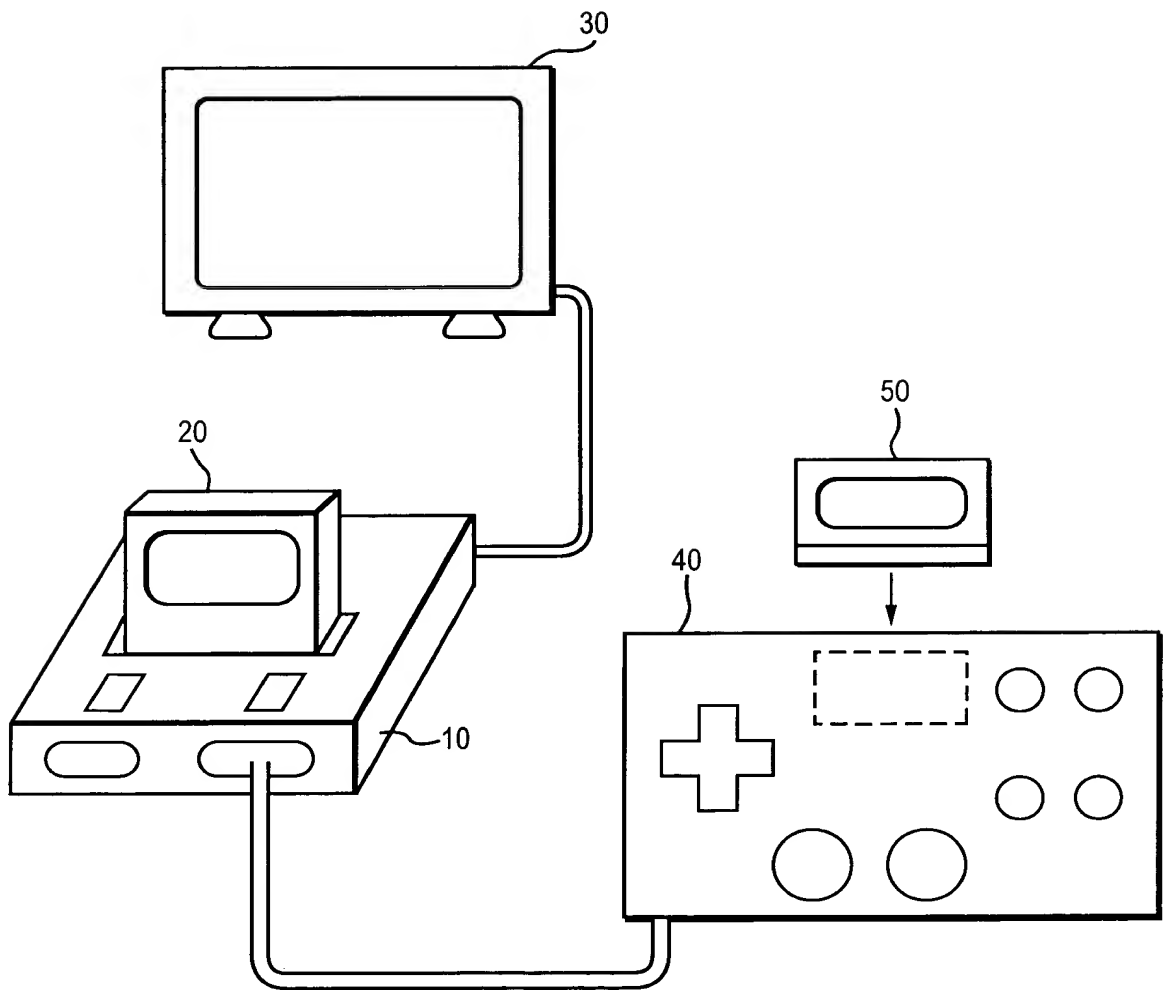
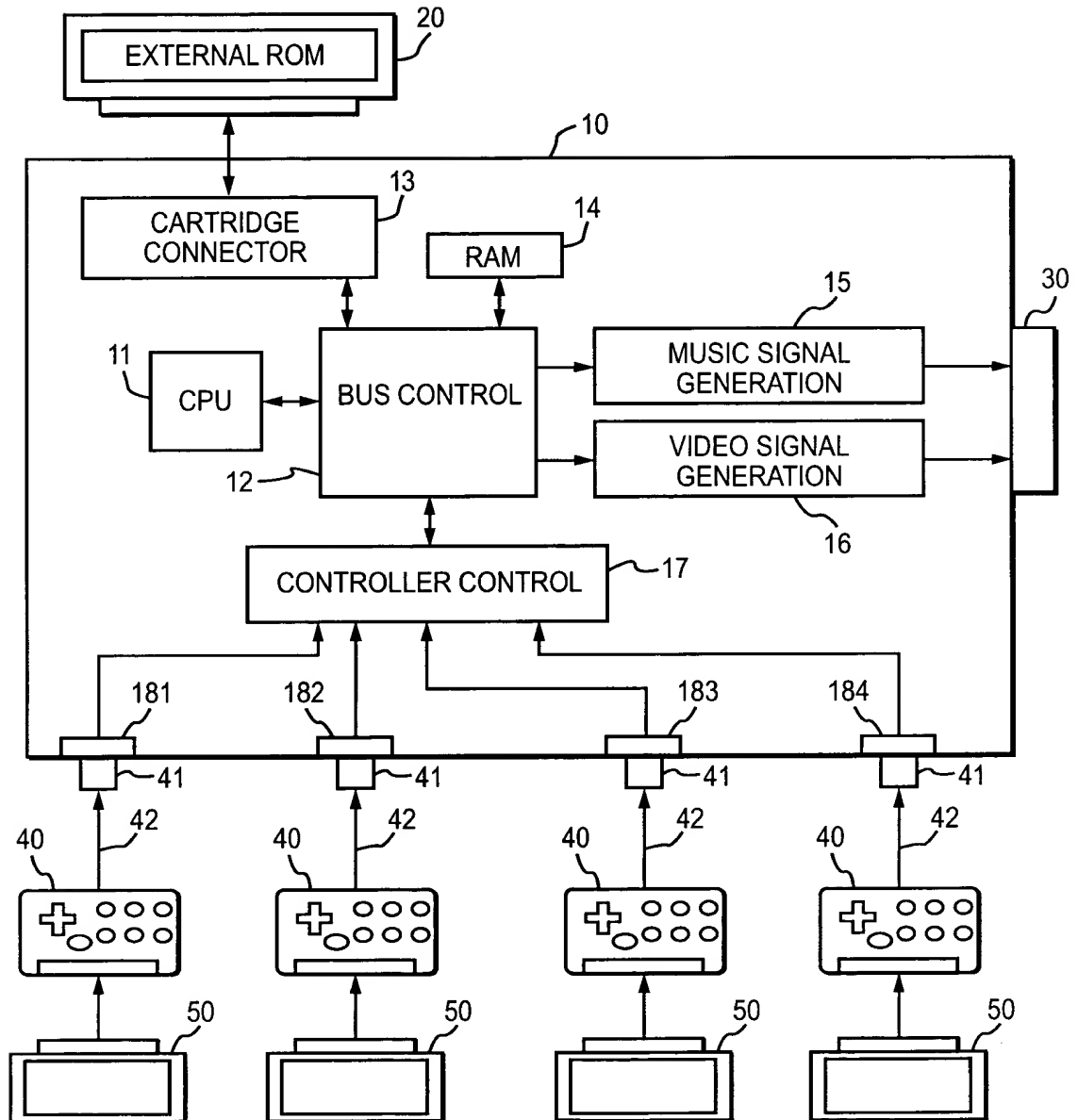




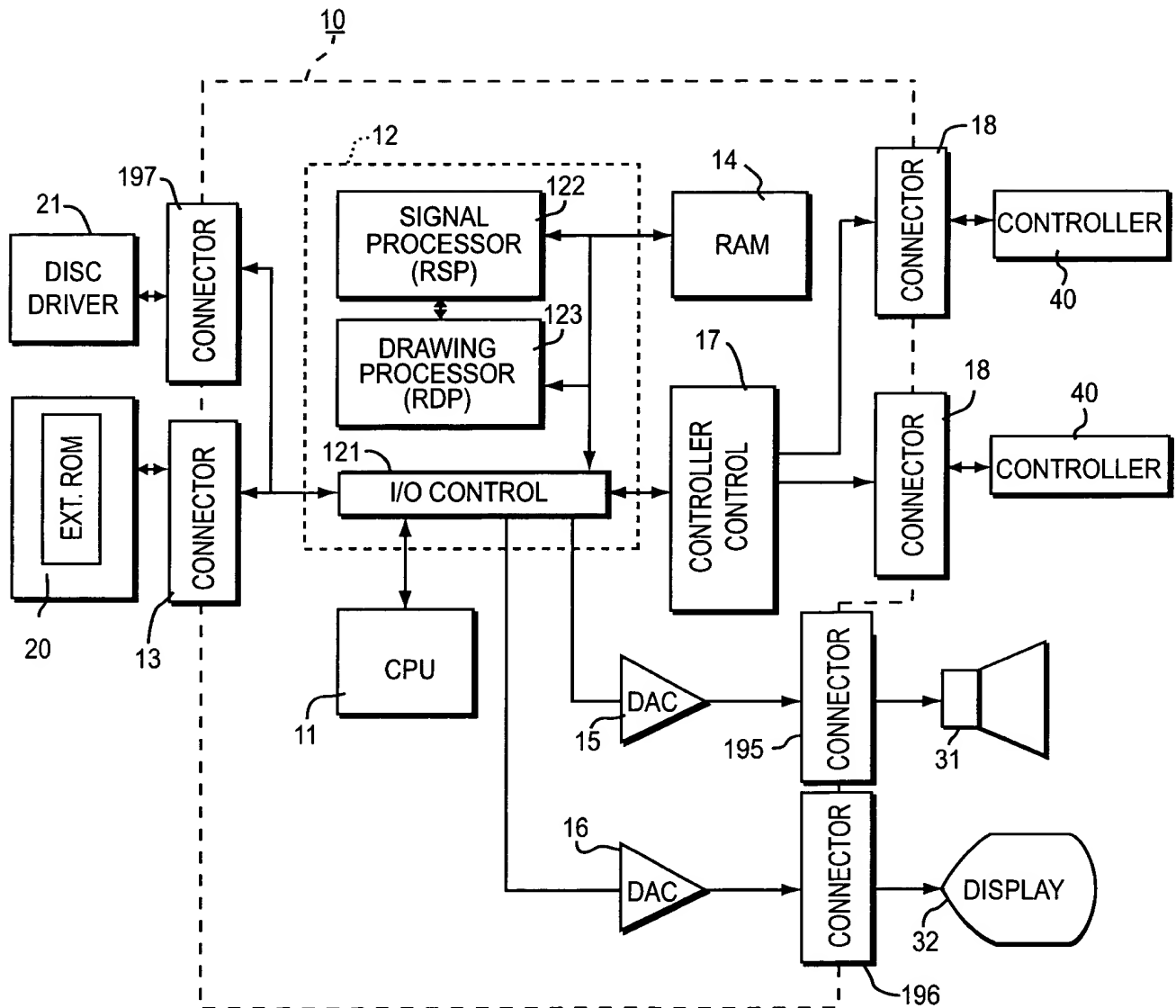
**Fig. 1**



**Fig. 2**

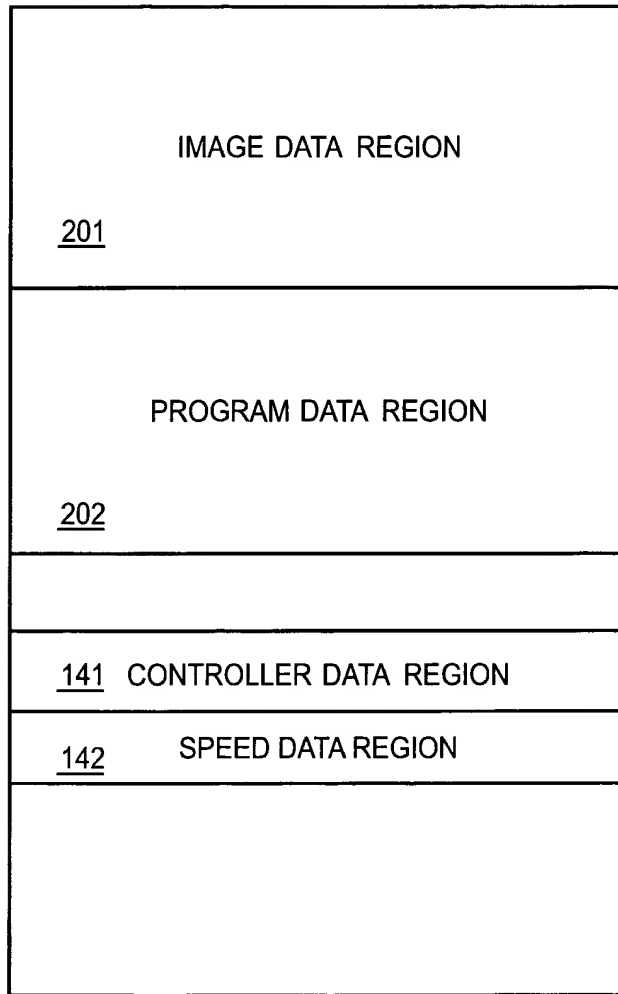


**Fig. 3**

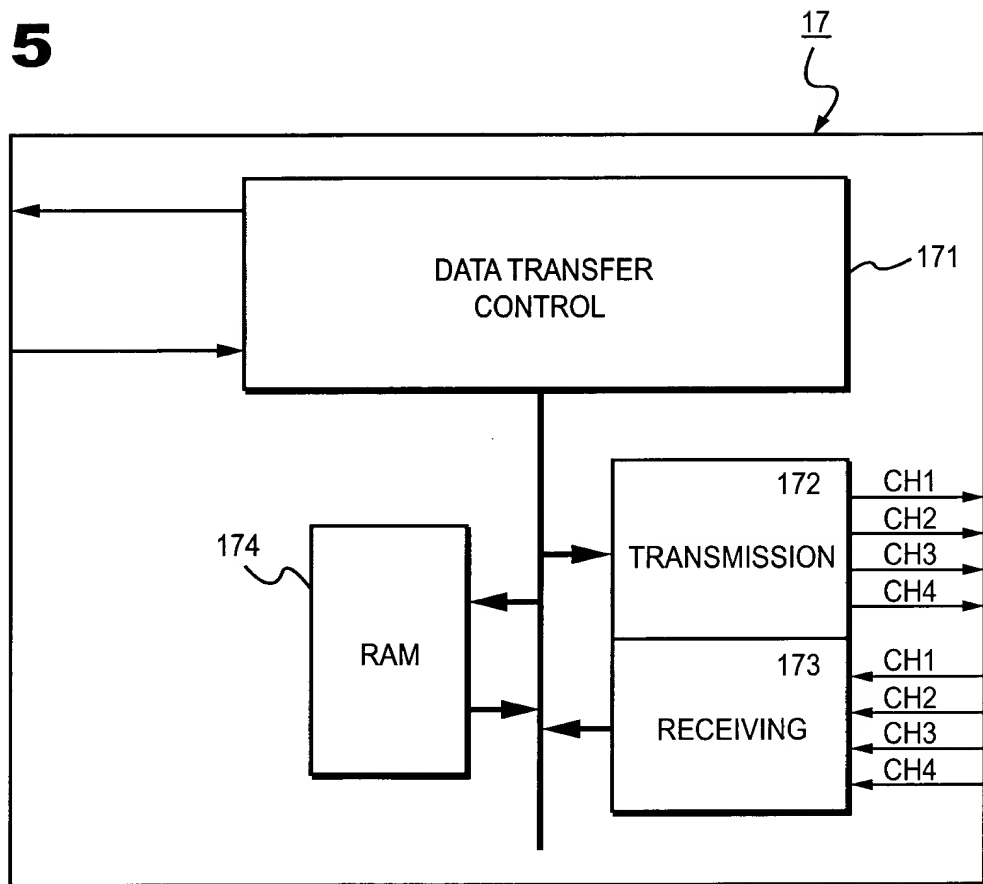


## Fig. 4

RAM 14

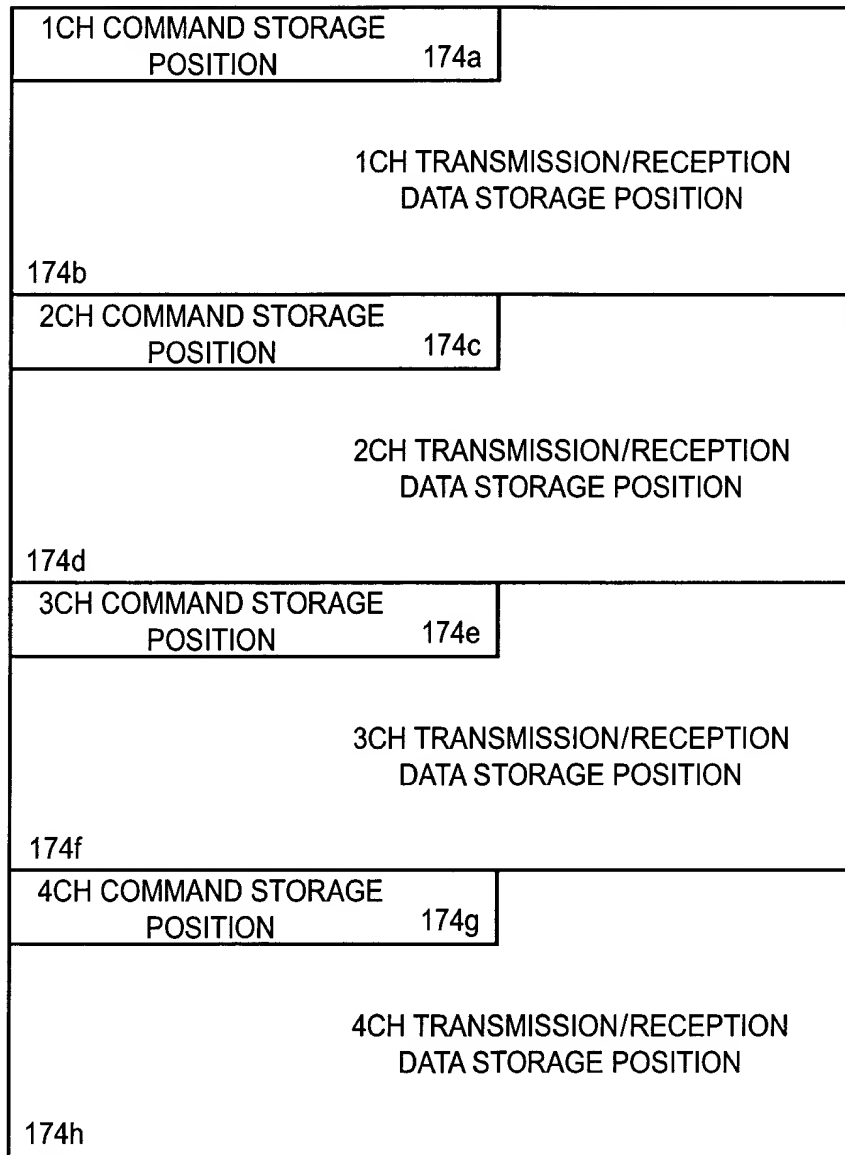


**Fig. 5**

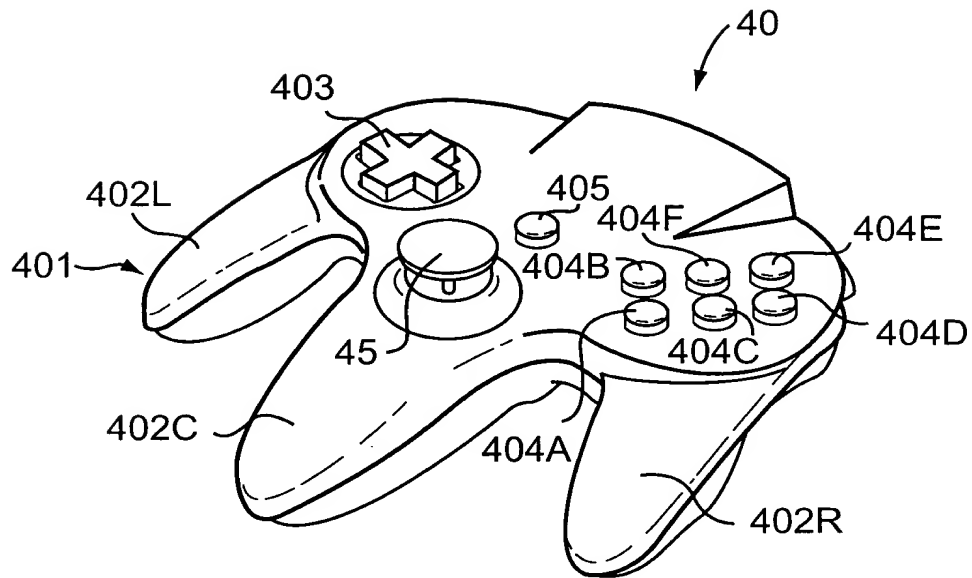


**Fig. 6**

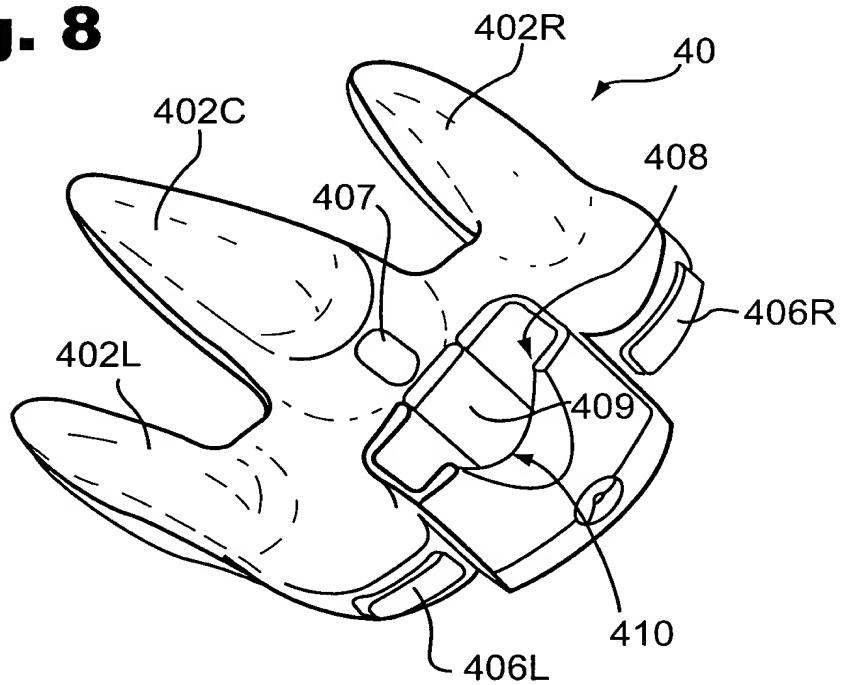
RAM 174



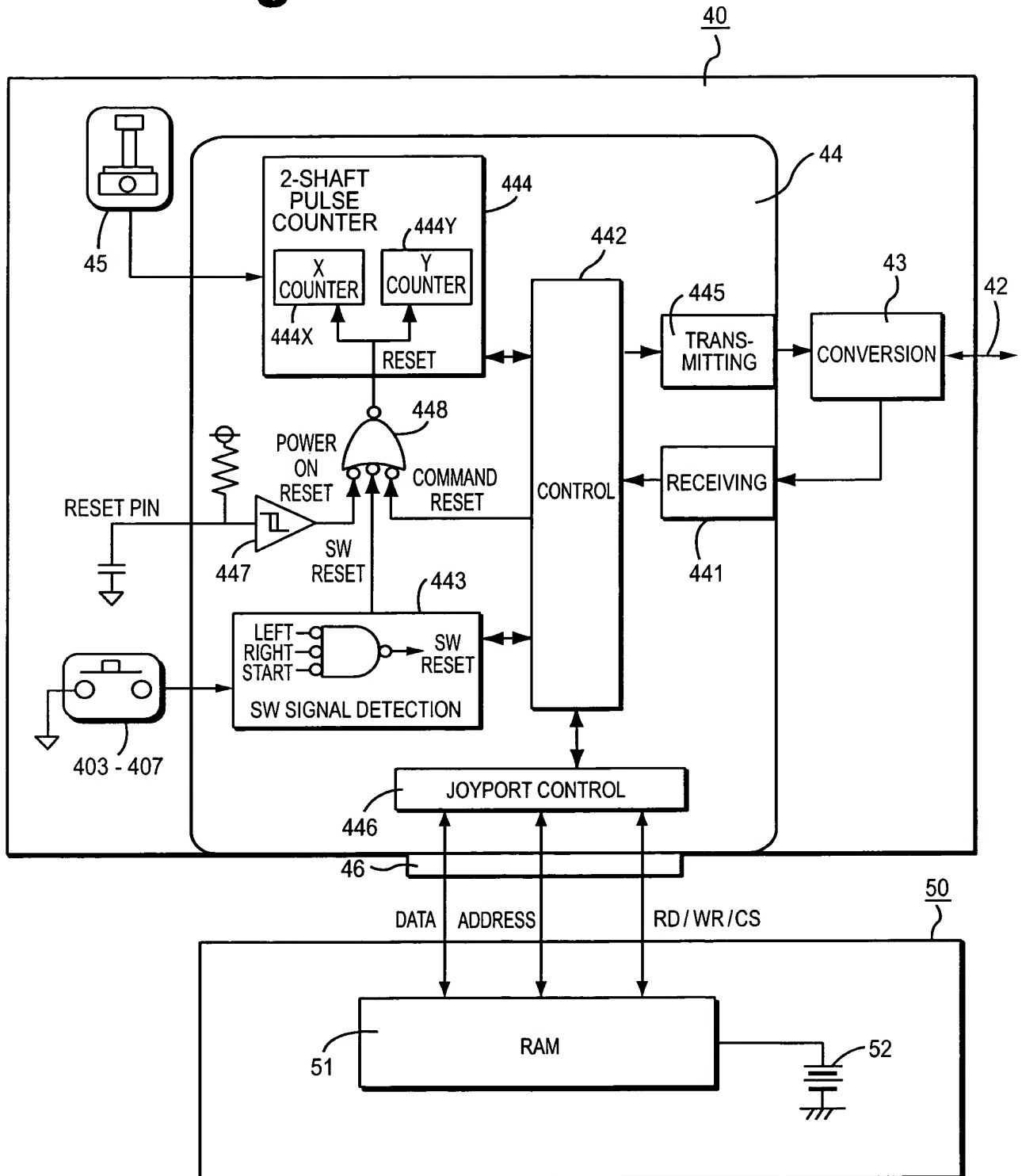
**Fig. 7**



**Fig. 8**



**Fig. 9**

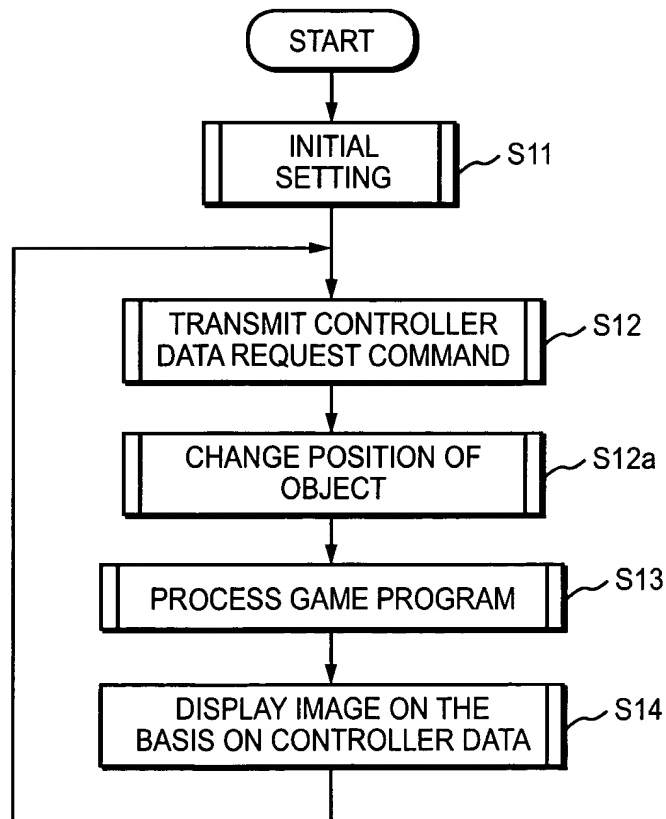




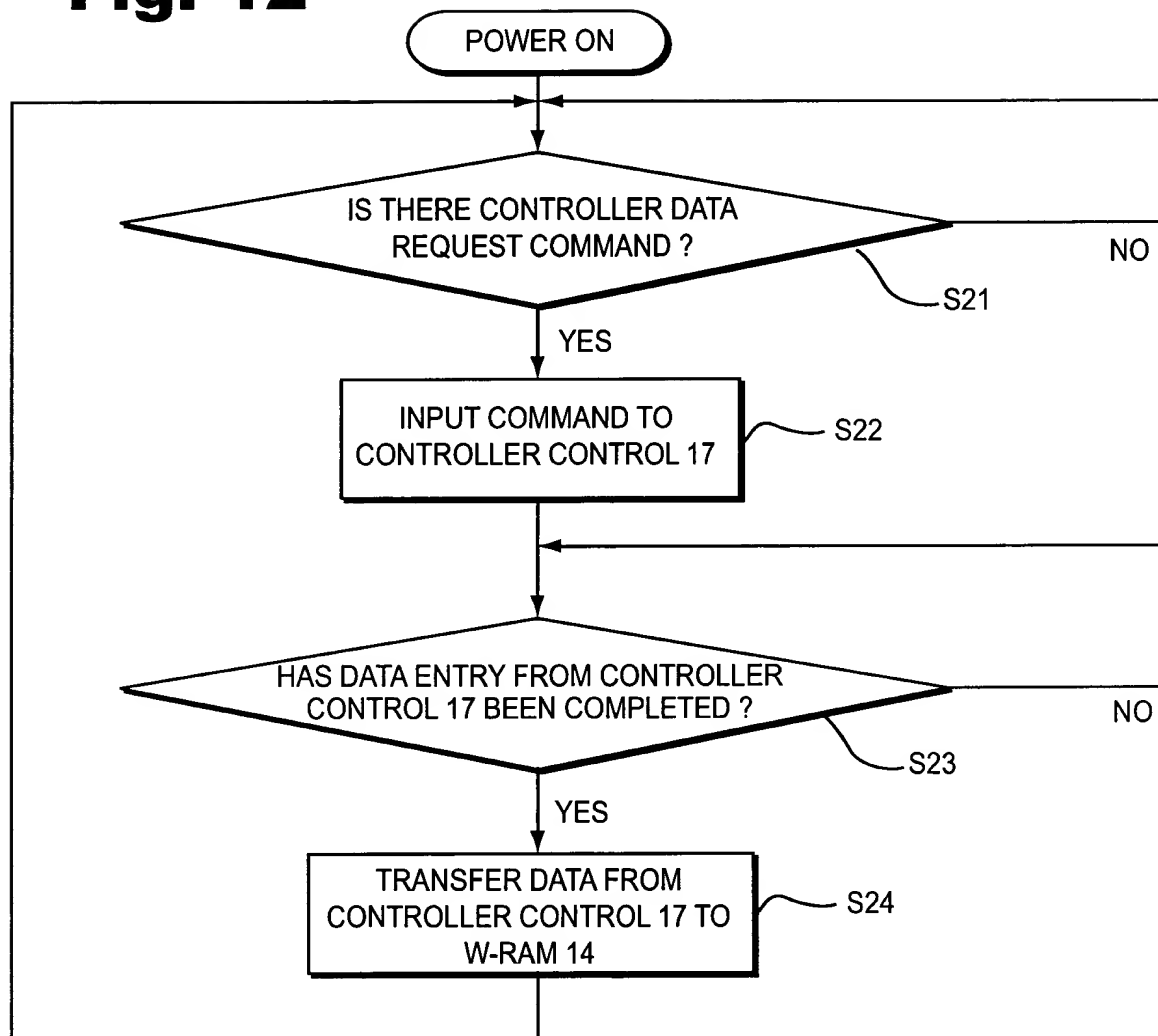
**Fig. 10**

1 BYTE	B	A	G	START	↑	↓	←	→
2 BYTE	JSRST	O	L	R	E	D	C	F
3 BYTE	X ORDINATE							
4 BYTE	Y ORDINATE							

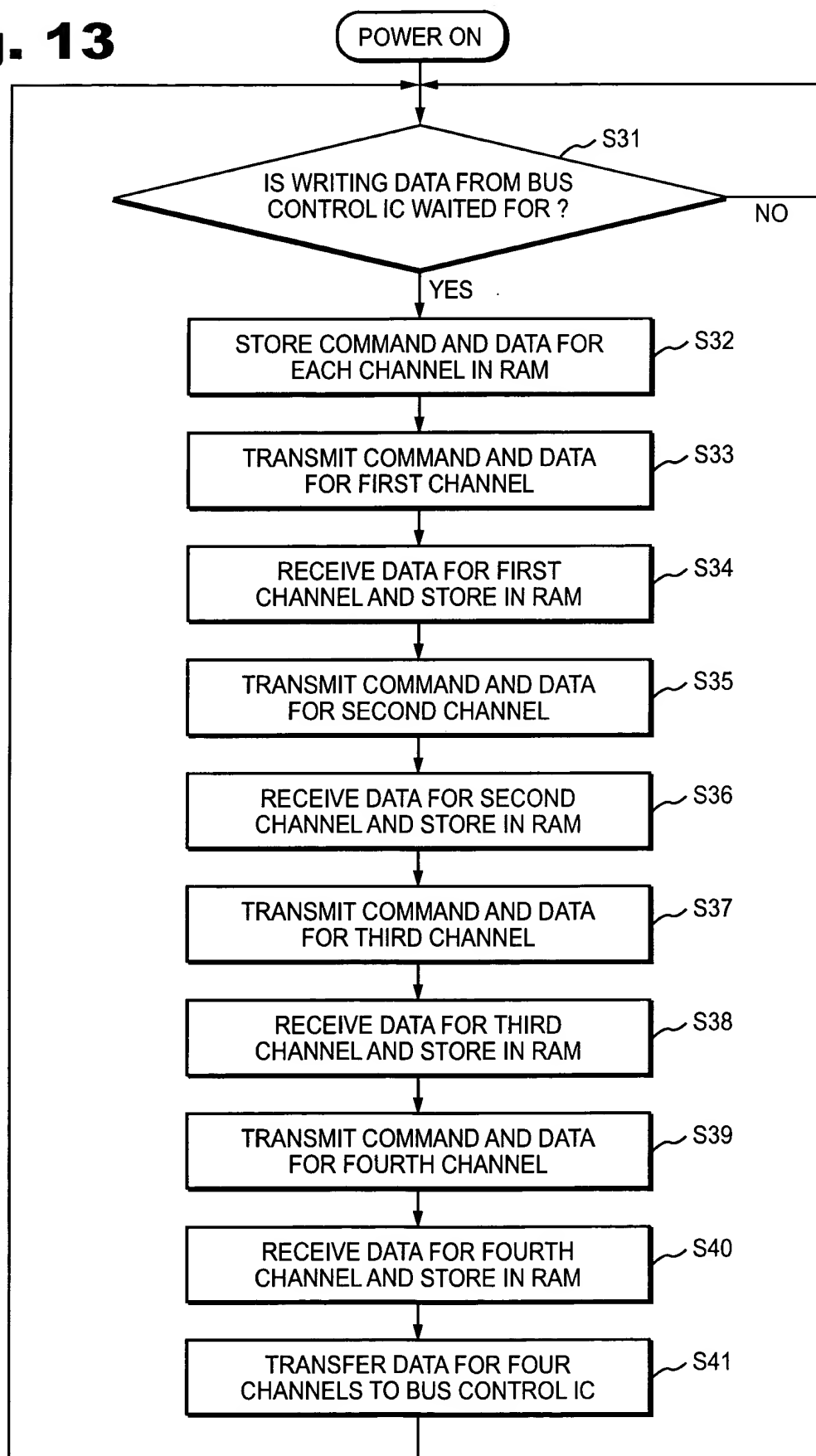
**Fig. 11**



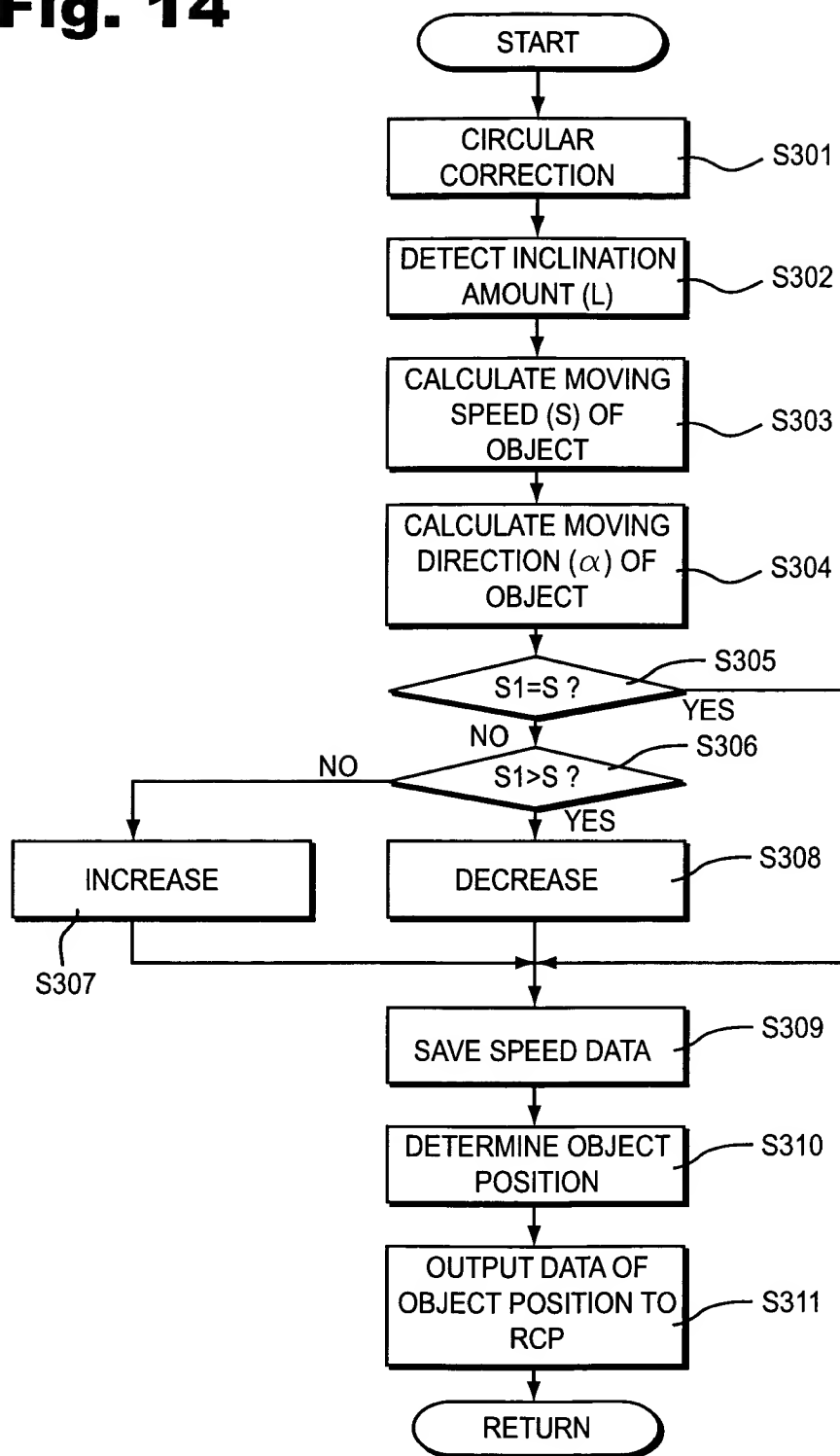
**Fig. 12**



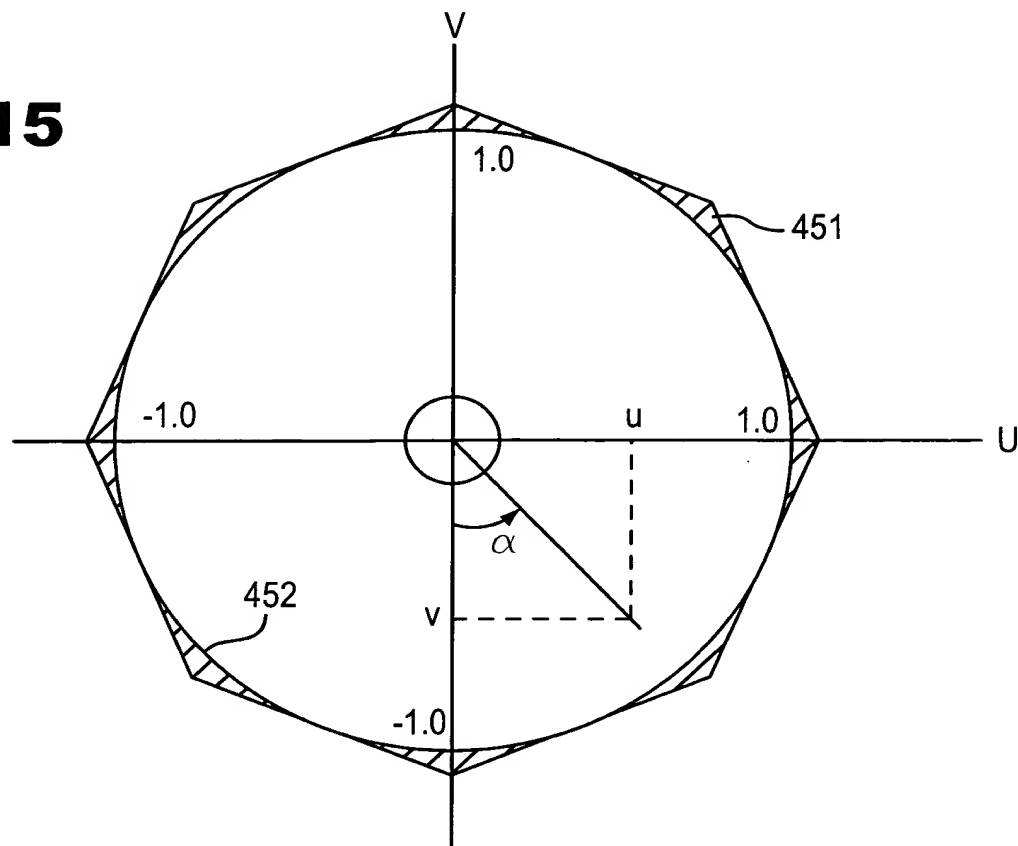
**Fig. 13**



**Fig. 14**



**Fig. 15**



**Fig. 16**

